


IC CARD AND MEMORY PACKAGE

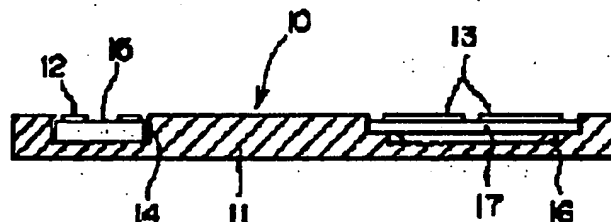
Patent number: JP10334205
Publication date: 1998-12-18
Inventor: OHARA MINORU
Applicant: TOSHIBA CORP
Classification:
- International: G06K19/077; G06K17/00; G06K19/073
- european:
Application number: JP19970141907 19970530
Priority number(s):

Also published as:

 JP10334205 (A)**Abstract of JP10334205**

PROBLEM TO BE SOLVED: To allow the single memory package to conduct arithmetic processing and to record lots of capacity of data by providing an IC and a flush memory in a base card.

SOLUTION: A recessed part 14 on the front side of a base card 11 is formed at a position corresponding to the position of the connector of a conventional IC card, and an IC 15 is mounted on the recessed part 14. The IC 15 is a one-chip integrated circuit that integrates storage, arithmetic and control functions required for desired information processing such as password processing and encryption processing. The IC 15 is provided with a security circuit to prevent illegal write and forgery of data and pluralities of contact terminals for data input output and they are exposed on the front side of the base card 11 and extended almost over the front side. The IC 15 is accessed by an IC card reader/writer via a terminal array. A simultaneous deletion flush memory 17 is provided onto a recessed part 16 to store large capacity of data. A contact terminal 13 is provided for the memory 17 and exposed on the same contact face as that of the IC 15 and extended almost all over of the front side.



Data supplied from the esp@cenet database - Patent Abstracts of Japan**BEST AVAILABLE COPY**